

N.D.  
10/4/01  
Bog-Auditt

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yukiro Kashima : Art Unit:  
Serial No.: To be assigned : Examiner:  
Filed: Herewith :  
FOR: MICROWAVE OSCILLATOR AND :  
LOW-NOISE CONVERTER USING THE :  
SAME :

## PRELIMINARY AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

SIR:

Prior to examination, please amend the above application as follows:

IN THE SPECIFICATION:

Please replace the paragraphs beginning at page 3, line 20:

A microwave oscillator is characterized by the configuration in which MSL A released at one end is connected to the base terminal of a transistor, MSL B is connected to the collector terminal, a DR is disposed closely to the MSL A and MSL B to couple them electromagnetically, and a stabilized oscillation output is obtained by inducing parallel feedback from the collector to the base, and further the center of the DR is set closest to the position at which the distance from the released end on the MSL A is  $\lambda g 1/4$ , and HIL is merely connected to this position as a bias supply line to the base terminal, and therefore without using choke circuits which occupied a relatively wide area on the conventional circuit board, a small and stable microwave oscillator having excellent phase noise characteristics may be realized.